

INTERFERENCE SEARCH

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	((performance adj table\$1) and (processor\$1 or microprocessor\$1 or CPU\$1) and compar\$4 and (performance adj structure\$1)).clm.	US-PGPUB	OR	OFF	2006/01/26 20:29
L2	1	((performance adj table\$1) and (processor\$1 or microprocessor\$1 or CPU\$1) and compar\$4 and ((performance adj state\$1) near2 structure\$1)).clm.	US-PGPUB	OR	OFF	2006/01/26 20:25
L3	1	(boot\$3 and ((performance adj state\$1) near2 structure\$1) and (performance adj table\$1) and updat\$3).clm.	US-PGPUB	OR	OFF	2006/01/26 20:30
L4	0	((performance adj table\$1) and (processor\$1 or microprocessor\$1 or CPU\$1) and compar\$4 and (performance near2 structure\$1) and ((system adj management adj interrupt\$1) or SMI)).clm.	US-PGPUB	OR	OFF	2006/01/26 20:29
L5	1	(boot\$3 and ((performance adj state\$1) near2 structure\$1) and (performance adj table\$1) and updat\$3 and ((advanced adj configuration adj2 power adj management) or ACPI)).clm.	US-PGPUB	OR	OFF	2006/01/26 20:31